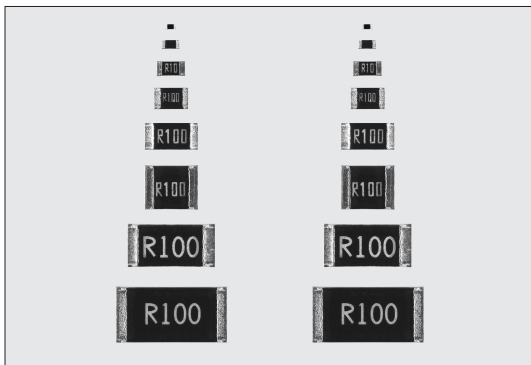


THICK FILM (LOW RESISTANCE)



SR73 矩形低电阻片式电阻器 Low Resistance Flat Chip Resistors



外观颜色: 黑色(1H) 紫色(1E, 1J, 2A, 2B, 2E, W2H, W3A)
Coating color: Black (1H)
Indigo (1E, 1J, 2A, 2B, 2E, W2H, W3A)

特点 Features

- 是电源电路、电动机电路等的电流检测电阻器。
- 是阻值允许偏差±0.5%、电阻温度系数±100×10⁻⁶/K的高信赖性、高性能产品。
- 对应回流焊、波峰焊。
- 端子无铅电镀品,符合欧盟RoHS。电极、电阻膜层、玻璃中所含的铅玻璃不适用欧盟RoHS指令。
- AEC-Q200相关数据已取得(除1H)。
- Current detecting resistors for power supply, motor circuits, etc.
- High reliability and performance with resistance tolerance ±0.5%, T.C.R. ±100×10⁻⁶/K
- Suitable for both flow and reflow solderings.
- Products with lead free termination meet EU-RoHS requirements. EU-RoHS regulation is not intended for Pb-glass contained in electrode, resistor element and glass.
- AEC-Q200 qualified (Exemption 1H).

用途 Applications

- 电子计算机、HDD、手机、电源、电动机等。
- Computers, HDDs, Cellular-telephones, Power supplies, and Motor circuits, etc.

品名构成 Type Designation

实例 Example

SR73	2B	T	TD	R10	J
品种 Product Code	额定功率 Power Rating	端子表面材质 Terminal Surface Material	二次加工 Taping	公称电阻值 Nominal Resistance	阻值允许偏差 Resistance Tolerance
	1H:0.1W 1E:0.125W 1J:0.2W 2A:0.25W 2B:0.33W 2E:0.5W W2H:0.75W W3A:1.0W	T:Sn G:Au ^{※3} (L:Sn/Pb)	TCM:2mm pitch press paper TPL-TP:2mm pitch punch paper TD:4mm pitch punch paper TE:4mm pitch plastic embossed BK:Bulk	D,F:4 digits G,J:3 digits Ex. ^{※2} 0.1Ω:R100 47mΩ:47L	D:±0.5% F:±1% G:±2% J:±5%

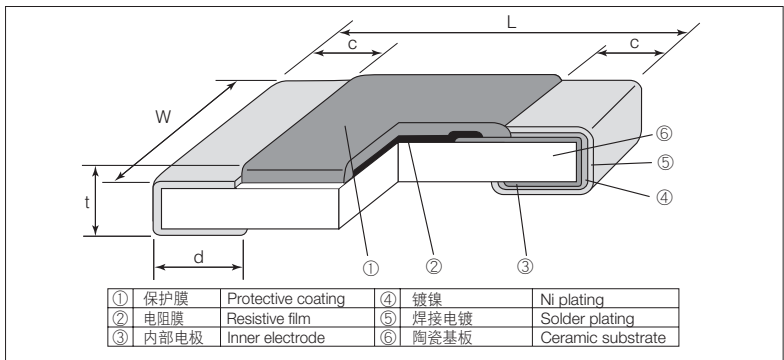
※2 电阻值范围 (Ω) Resistance Value		3位显示 3 digits		电阻值范围 (Ω) Resistance Value		4位显示 4 digits	
24m~91m		24L~91L		0.1~0.976		R100~R976	
0.1~0.91		R10~R91		1~9.76		1R00~9R76	
1~9.1		1R0~9R1		10		10R0	

- ※3 镀金电极品,以1J、2A、2B (0.1Ω~10Ω) 对应。由于规格有一定不同,请与本公司商谈。
※3 Products with gold plated electrodes are also available only 1J, 2A and 2B type (0.1Ω~10Ω), so please consult with us.
※6 1H及W2H、W3A只对应端子表面材质T。
※6 With type 1H, W2H and W3A, only T is available as the terminal surface material.
端子表面材质,以无铅品为准。
欲知关于此产品含有的环境负荷物质详情(除EU-RoHS以外),请与我们联系。
编带细节请参考卷末附录C。
The terminal surface material lead free is standard.
Contact us when you have control request for environmental hazardous material other than the substance specified by EU-RoHS.
For further information on taping, please refer to APPENDIX C on the back pages.

参考标准 Reference Standards

IEC 60115-8
JIS C 5201-8
EIAJ RC-2134C

结构图 Construction

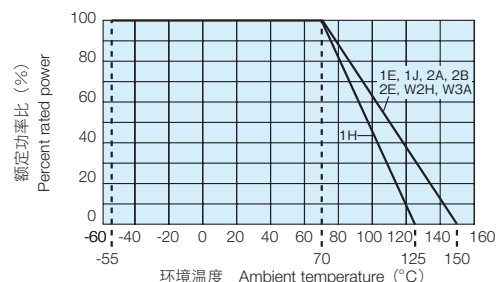


外形尺寸 Dimensions

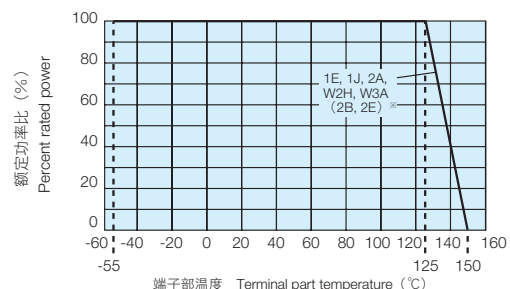
型号 Type (Inch Size Code)	尺寸 Dimensions (mm)					Weight (g) (1000pcs)
	L	W	c	d	t	
1H (0201)	0.6±0.03	0.3±0.03	0.1±0.05	0.15±0.05	0.23±0.03	0.14
1E (0402)	1.0 ^{+0.1} _{-0.05}	0.5 ^{+0.1} _{-0.05}	0.25±0.1	0.25±0.1	0.35±0.05	0.68
1J (0603)	1.6±0.2	0.8 ^{+0.1} _{-0.15}	0.35±0.1	0.35±0.1	0.45±0.1	2.14
2A (0805)	2.0±0.2	1.25±0.1	0.4±0.2	0.3 ^{+0.2} _{-0.1}	0.5±0.1	4.54
2B (1206)	3.2±0.2	1.6±0.2	0.5±0.3	0.4 ^{+0.2} _{-0.1}	0.6±0.1	9.14
2E (1210)		2.6±0.2				15.5
W2H (2010) ^{※1}	5.0±0.2	2.5±0.2	0.65±0.15			24.3
W3A (2512) ^{※1}	6.3±0.2	3.1±0.2				37.1

※1 SR73 2H和SR73 3A也对应 (“d” 尺寸不同。“d” 尺寸=0.4^{+0.2}_{-0.1} mm)
※1 SR73 2H and SR73 3A are also still available (different “d” dimensions=0.4^{+0.2}_{-0.1} mm)

负荷减轻特性曲线 Derating Curve



在环境温度70℃以上使用时,应按照上图负荷减轻特性曲线,减小额定功率。
For resistors operated at an ambient temperature of 70°C or above, a power rating shall be derated in accordance with the above derating curve.



超过上述端子部温度使用时,请根据负荷减轻特性曲线减小额定功率后使用。有关使用方法,请参照卷首的“端子部温度负荷减轻特性曲线的说明”。
※有关2B、2E端子部温度的负荷减轻特性曲线,请与我们联系。
For resistors operated terminal part temperature of described for each size or above, a power rating shall be derated in accordance with derating curve. Please refer to “Introduction of the derating curves based on the terminal part temperature” on the beginning of our catalog before use.
※Please inquire us about the derating curves of terminal part temperature for 2B and 2E.

■ 额定值 Ratings

型号 Type	电阻温度系数 T.C.R. ($\times 10^{-6}/K$)	额定功率 Power Rating	电阻值范围 Resistance Range (Ω)				额定环境温度 Rated Ambient Temp.	额定端子部温度 Rated Terminal Part Temp.	编带和包装数/卷 Taping & Q'ty/Reel (pcs)					
			G: $\pm 0.5\%$ E24 • E96	F: $\pm 1\%$ E24 • E96	G: $\pm 2\%$ E24	J: $\pm 5\%$ E24			TCM	TPL•TP	TD	TE		
1H ^{※4}	0~+500	0.1W	-	-	-	0.18~0.24	70℃	125℃ ^{※6}	TCM: 15,000	-	-	-		
	0~+400		-	1~10	-	0.27~10								
1E ^{※4}	±200	0.125W (0.166W ^{※5})	-	0.51~10	0.51~10	0.51~10			-	-	TPL: 20,000 TP: 10,000	-	-	-
	±300		-	0.2~0.47	0.2~0.47	0.2~0.47								
	±500		-	0.1~0.18	0.1~0.18	0.1~0.18								
1J	±200	0.2W	-	0.1~10	0.1~10	0.1~10			-	-	10,000	5,000	-	-
	±100		0.15~10	0.1~10	-	-								
2A	±200	0.25W (0.33W ^{※5})	-	-	0.1~10	0.1~10			-	-	10,000	5,000	4,000	-
	±500		-	-	-	0.051~0.091								
	±800		-	-	-	0.030~0.047								
	±100		0.15~10	0.1~10	-	-								
2B	±200	0.33W (0.5W ^{※5})	-	-	0.1~10	0.1~10			-	-	-	-	5,000	4,000
	±500		-	-	-	0.056~0.091								
	±800		-	-	-	0.030~0.051								
	±100		-	0.1~10	-	-								
2E	±200	0.5W (0.66W ^{※5})	-	-	0.1~10	0.047~10	-	-	-	-	5,000	4,000		
	±500		-	-	-	0.036~0.043								
	±1000		-	-	-	0.024~0.033								
	±100		-	0.1~10	-	-								
W2H	±200	0.75W	-	-	0.1~10	0.1~10	-	-	-	-	-	4,000		
	±500		-	-	-	0.056~0.091								
	±800		-	-	-	0.033~0.051								
	±100		-	0.1~10	-	-								
W3A	±200	1.0W	-	-	0.1~10	0.1~10	-	-	-	-	-	4,000		
	±500		-	-	-	0.056~0.091								
	±800		-	-	-	0.039~0.051								
	±100		-	0.1~10	-	-								

使用温度范围 Operating Temperature Range: -55℃~+125℃ (1H), -55℃+150℃ (1E, 1J, 2A, 2B, 2E, W2H, W3A)

额定电压是 $\sqrt{\text{额定功率} \times \text{公称电阻值}}$ 所算出的值。

Rated voltage = $\sqrt{\text{Power Rating} \times \text{Resistance value}}$.

※4SR731H, SR731E (F: $\pm 1\%$) 的公称电阻值只成为E24系列。 ※4 The nominal resistance value for SR731H, SR731E (F: $\pm 1\%$) is in E24.

※5的额定功率使用时, 请与我们联系。 Please inquire of us if the component is used at ※5 marked power.

※6按2B、2E的额定端子部温度使用时, 请您另行询问。 ※6 Please inquire of us if 2B and 2E are used by rated terminal part temperature

■ 性能 Performance

试验项目 Test Items	标准值 Performance Requirements $\Delta R \pm (\% + 0.005 \Omega)$		试验方法 Test Methods
	保证值 Limit	代表值 Typical	
电阻值 Resistance	在规定的允许偏差内 Within specified tolerance		25℃
电阻温度系数 T.C.R.	在规定的值以内 Within specified T.C.R.		+25℃/-55℃ and +25℃/+125℃
过载 (短时间) Overload (Short time)	2	0.5	额定电压 $\times 2.5$ 倍施加5秒钟 Rated voltage $\times 2.5$ for 5s
耐焊接热 Resistance to soldering heat	3: 1H 1: 1E~W3A	0.75: 1H 0.3: 1E~W3A	260℃ $\pm 5\%$, 10s $\pm 1s$
温度突变 Rapid change of temperature	1	0.3	-40℃ (30min.) / +125℃ (30min.) 100 cycles
耐湿负荷 Moisture resistance	3: 1H 2: 1E~W3A	1	40℃ $\pm 2\%$, 90%~95%RH, 1000h 1.5小时ON、0.5小时OFF的周期 1.5h ON/0.5h OFF cycle
在70℃时的耐久性 Endurance at 70℃	3: 1H 2: 1E~W3A	1	70℃ $\pm 2\%$, 1000h 1.5小时ON、0.5小时OFF的周期 1.5h ON/0.5h OFF cycle
高温放置 High temperature exposure	1	0.3	+125℃, 1000h: 1H +150℃, 1000h: 1E, 1J, 2A, 2B, 2E, W2H, W3A

■ 使用注意事项 Precautions for Use

- 片状电阻器的基材是氧化铝。由于和安装基板的热膨胀系数不同, 在反复提供热循环等热应力时, 结合部的焊锡 (焊缝部) 有时会发生裂纹。特别是W2H/W3A的大型情况, 由于热膨胀大, 而且自身发热也大, 环境温度的变动有大的反复和载荷启闭有反复时, 需要注意裂纹的发生。用环氧树脂印刷电路板 (FR-4) 作一般性热循环试验, 在使用温度范围的上下限进行时, 1E~2E的类型, 不容易发生裂纹, 而W2H/W3A型, 则有易于发生裂纹的倾向。由于热应力而发生裂纹, 取决于安装的焊接区的大小、焊接量、安装基板的散热性等。因此, 在环境温度有大的变化和载荷 ON / OFF 使用条件时, 应充分注意后进行设计。
- 根据焊盘图案的大小和接续焊接的量, 焊接后的电阻值会有变动。应事前在确认电阻值降低·上升的影响后, 进行设备设计。
- The substrate of chip resistors is alumina. Cracks may occur at the connection of solder (solder fillet portion) due to the difference of the coefficient of thermal expansion from a mounting board when heat stress like heat cycle, etc. are repeatedly given to them. Care should be taken to the occurrence of the cracks when the change in ambient temperature or ON / OFF of load is repeated, especially when large types of W2H/W3A which have large thermal expansion and also self heating. By general temperature cycle test using glass-epoxy (FR-4) boards under the maximum/minimum temperatures of operating temperature range, the crack does not occur easily in the types of 1E~2E, but the crack tends to occur in the types of W2H/W3A. The occurrence of the crack by heat stress may be influenced by the size of a pad, solder volume, heat radiation of mounting board etc., so please pay careful attention to designing when a big change in ambient temperature and conditions for use like ON/OFF of load can be assumed.
- The resistance value after soldering may change depending on the size of pad pattern or solder amount. Make sure the effect of decline/increase of resistance value before designing.