iC-LSC 12-CHANNEL ACTIVE PHOTOSENSOR ARRAY



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FEATURES

Monolithic array of independent photosensors with excellent matching Compact photosensor size of 800 µm x 300 µm enabling

high-quality encoder scanning at reduced system dimensions Narrow track pitch of 0.42 mm cuts down illumination efforts Enhanced EMI immunity due to on-chip pre-amplification Dark current compensation permits high temperature operation

Open-collector outputs as highside current source Simple gain setting and current-to-voltage conversion by external load resistors

Single supply operation from 4 V to 5.5 V

Low power consumption

Space saving, RoHS compliant optoQFN and optoBGA packages

Options: extended temperature range of -40 °C to 125 °C, customized COB modules, reticles and code discs

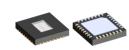
APPLICATIONS

Optical position encoding from analog sine/cosine signals Incremental encoders with index signal

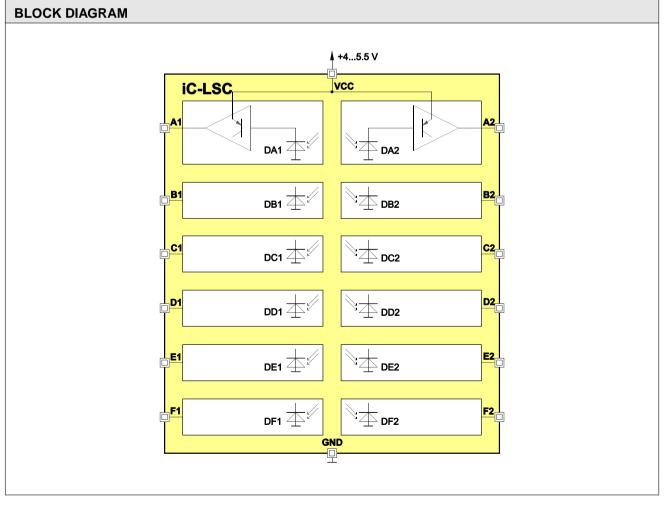
Motor commutation encoders

PACKAGES





14-pin optoBGA 6.2 mm x 5.2 mm RoHS compliant 32-pin optoQFN 5 mm x 5 mm / 0.9 mm RoHS compliant



iC-LSC 12-CHANNEL ACTIVE PHOTOSENSOR ARRAY



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DESCRIPTION

The iC-LSC sensor array, coming with 12 independent channels, is a general purpose optoelectronic scanner made to suit a variety of encoding applications, such as rotary and linear encoders used for motion control, robotics, brushless DC motor commutation, power tools etc.

The sensor array features monolithically integrated photosensors with active areas of $800 \,\mu m \times 300 \,\mu m$ each in combination with fast on-chip photocurrent amplifiers, enabling an analog output at reasonable signal strength to the circuit board.

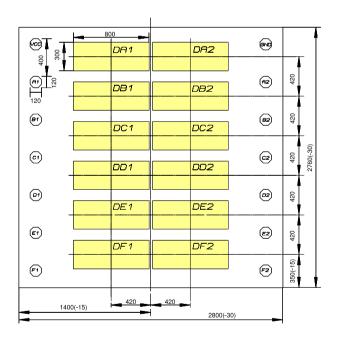
The highside current source output construction avoids a ground referenced signal and permits the subsequent electronics to adjust the gain. In its simplest form this is done by load resistors, for instance.

The spectral sensitivity range includes visible to near infrared light, with the maximum sensitivity being close to a wavelength of 700 nm.

Output currents of up to 50 μ A are supplied under low light conditions, for instance when illuminated at only 3μ W/mm² by an 850 nm LED. The photocurrent gain is 46 dB typically.

PACKAGING INFORMATION

PAD LAYOUT Chip size 2.80 mm x 2.76 mm



PAD FUNCTIONS No. Name Function

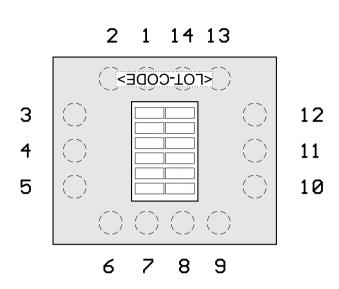
1	VCC	+45.5 V Supply Voltage
2	A1	Highside Current Source Output
3	B1	Highside Current Source Output

- 4 C1 Highside Current Source Output
- D1 Highside Current Source Output
- 5 D1 Highside Current Source Output
- 6 E1 Highside Current Source Output
- 7 F1 Highside Current Source Output
- 8 F2 Highside Current Source Output
- 9 E2 Highside Current Source Output
- 10 D2 Highside Current Source Output
- 11 C2 Highside Current Source Output
- 12 B2 Highside Current Source Output
- 13 A2 Highside Current Source Output
- 14 GND Ground



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PIN CONFIGURATION oBGA LS2C (6.2 mm x 5.2 mm)

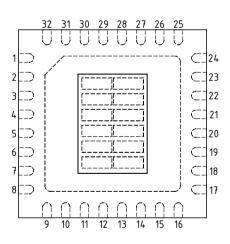


PIN FUNCTIONS No. Name Function

NO .	name	Function
1	VCC	+45.5 V Supply Voltage
2	A1	Highside Current Source Output
3	B1	Highside Current Source Output
4	C1	Highside Current Source Output
5	D1	Highside Current Source Output
6	E1	Highside Current Source Output
7	F1	Highside Current Source Output
8	F2	Highside Current Source Output
9	E2	Highside Current Source Output
10	D2	Highside Current Source Output
11	C2	Highside Current Source Output
12	B2	Highside Current Source Output
13	A2	Highside Current Source Output
14	GND	Ground

For dimensional specifications refer to the relevant package data sheet, available separately. IC top markings, such as <PRODUCT ID>, <ASSY CODE> or <LOT CODE>, indicate the orientation of the device.

PIN CONFIGURATION oQFN32-5x5 (5 mm x 5 mm)



PIN FUNCTIONS

Name Function No.

	VCC n c ¹⁾	+45.5 V Supply Voltage
-		
•	A1	Highside Current Source Output
4	B1	Highside Current Source Output
5	C1	Highside Current Source Output
6	D1	Highside Current Source Output
7	E1	Highside Current Source Output
8	F1	Highside Current Source Output
916	n.c.	
17	F2	Highside Current Source Output
18	E2	Highside Current Source Output
19	D2	Highside Current Source Output
20	C2	Highside Current Source Output
21	B2	Highside Current Source Output
22	A2	Highside Current Source Output
23	n.c.	-
24	GND	Ground
2532	n.c.	
	BP	Backside Paddle ²⁾

Pin numbers marked n.c. are not in use.
The backside paddle may have a single link to GND. A current flow across the paddle is not permissible.

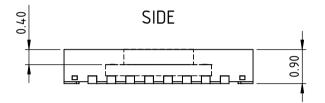


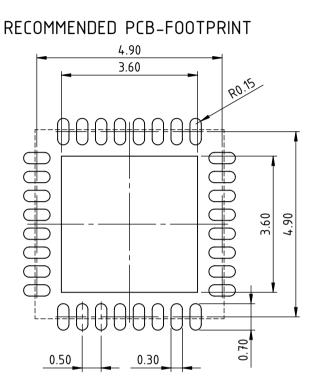
PACKAGE DIMENSIONS oQFN32-5x5

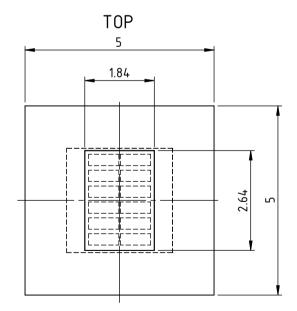
All dimensions given in mm.

Maximum molding excess +20 μm / -75 μm versus surface of glass/reticle.

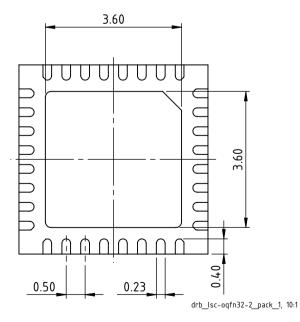
Maximum package thickness tolerance +/- 0.1 mm.







BOTTOM





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ABSOLUTE MAXIMUM RATINGS

These ratings do not imply operating conditions; functional operation is not guaranteed. Beyond these ratings device damage may occur.

Item	Symbol	Parameter	Conditions			Unit
No.	-			Min.	Max.	
G001	VCC	Voltage at VCC		-0.3	6	V
G002	I(VCC)	Current in VCC		-20	20	mA
G003	V()	Pin Voltage, all signal outputs		-0.3	VCC+0.3	V
G004	I()	Pin Current, all signal outputs		-20	20	mA
G005	Vd()	ESD Susceptibility, all pins	HBM, 100 pF discharged through 1.5 k Ω		2	kV
G006	Tj	Junction Temperature		-40	150	°C
G007	Ts	Chip Storage Temperature Range		-40	150	°C

THERMAL DATA

ltem	Symbol	Parameter	Conditions		0		Unit
No.				Min.	Тур.	Max.	
T01	Та	Operating Ambient Temperature Range	package oBGA LS2C package oQFN32-5x5	-20 -40		90 110	ပံ ပံ
			(extended temperature range on request)				
T02	Ts	Storage Temperature Range	package oBGA LS2C package oQFN32-5x5	-30 -40		110 110	သိ သိ
T03	Tpk	Soldering Peak Temperature	package oBGA LS2C				
			tpk < 20 s, convection reflow tpk < 20 s, vapor phase soldering			245 230	℃ ℃
			TOL (time on label) 8 h; Please refer to customer information file No. 7 for details.				
T04	Tpk	Soldering Peak Temperature	package oQFN32-5x5				
			tpk < 20 s, convection reflow tpk < 20 s, vapor phase soldering			245 230	℃ ℃
			MSL 5A (max. floor live 24 h at 30 °C and 60 % RH); Please refer to customer information file No. 7 for details.				



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ELECTRICAL CHARACTERISTICS

ltem No.	Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Total	Device		1	I			
001	VCC	Permissible Supply Voltage		4		5.5	V
002	I(VCC)	Supply Current in VCC, dark	E() = 0 Tj = 27 °C		1.5	2	mA mA
003	I(VCC)	Supply Current in VCC	$\lambda_{\text{LED}} = \lambda pk$, E() = 0.1 mW/cm ² Tj = 27 °C		2.2	4	mA mA
004	Vc()hi	Clamp-Voltage hi at all pins	I() = 4 mA			11	V
005	Vc()lo	Clamp-Voltage lo at all pins	I() = -4 mA	-1.2		-0.3	V
Photo	sensors						
101	E()mxr	Permissible Irradiance	$\lambda_{\text{LED}} = \lambda pk$			0.2	mW/ cm ²
102	Aph()	Radiant Sensitive Area	0.8 mm x 0.3 mm per sensor		0.24		mm ²
103	λ ar	Spectral Application Range	Se(λ ar) = 0.25 x S(λ)max see Figure 1	400		950	nm
104	λpk	Peak Sensitivity Wavelength	see Figure 1		680		nm
105	$S(\lambda)$	Spectral Sensitivity	$\lambda_{LED} = \lambda pk$		0.45		A/W
Photo	current Am	plifiers					
201	lph()	Permissible Photocurrent Operating Range	per sensor	0		200	nA
202	η()r	Photo Sensitivity (light-to-voltage conversion ratio)	$\lambda_{\text{LED}} = 740 \text{nm}$	60		120	A/W
203	CR()	Photocurrent Gain	CR() = lout() / lph()	150	200	250	
204	fc()hi	Cut-off Frequency (-3 dB)		150	200		kHz
205	⊿lout()m	Channel Matching	deviation from mean value	-15		+15	%
206	⊿lout()m	Channel Cross Talk	only one photosensor illuminated at the same time		0		%
Curre	nt Source C	Dutputs	·				
301	Vout()	Permissible Output Voltage (Operating Range)		1		VCC - 1.5	V
302	lout()	Permissible Output Current	Vout() = 1 V VCC - 1.5 V VCC = 4.55.5 V, Vout() = 1 V VCC - 2 V	-50 -200			μA μA
303	tr(), tf()	Output Current Rise/Fall Time	Iph: 0 \rightarrow 100 nA, 1T settling (63%); Vout() = constant CL = 30 pF, RL() = 10 k Ω		0.7 0.8		μs μs
304	lout()0	Output Dark Current		-0.75		+0.75	μA

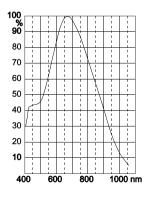


Figure 1: Relative spectral response

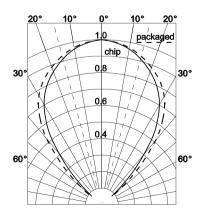


Figure 2: Typical directional characteristics

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APPLICATION CIRCUITS

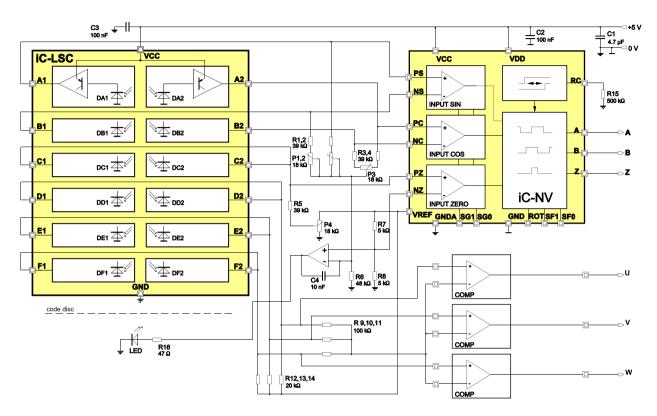


Figure 3: Optical encoder application example. Here, the sine-to-digital converter iC-NV is employed to output spike-free encoder quadrature signals featuring a minimum transition distance.

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ORDERING INFORMATION

Туре	Package	Options	Order Designation
iC-LSC	-		iC-LSC chip
	14-pin optoBGA 6.2 mm x 5.2 mm RoHS compliant	glass lid	iC-LSC oBGA LS2C
	14-pin optoBGA 6.2 mm x 5.2 mm RoHS compliant	on-chip reticle	iC-LSC oBGA LS2C-xR
	32-pin optoQFN 5 mm x 5 mm, thickness 0.9 mm RoHS compliant	glass lid	iC-LSC oQFN32-5x5
	14-pin optoBGA 6.2 mm x 5.2 mm	glass lid, leaded balls	iC-LSC oBGA LS2C-1

For technical support, information about prices and terms of delivery please contact:

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