## Application

- For Smartphones, Tablet computers, notebook PC, DSC, etc.


## Features

- External configuration size is $2.5 \times 2.0 \times 0.9 \mathrm{~mm}$ (weight of 0.02 g ), and is micro light weight.
- Low current consumption (Max. 0.7mA, @40MHz, +1.8V, No-load).
- Lead-free.



## Free

## RoHS Compliant Directive 2011/65/EU

Absolute maximum rating
Supply Voltage (Vcc) -0.3 to +4.0 V Storage Temperature Range -55 to $+125^{\circ} \mathrm{C}$

| - Specifications |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Item |  |  | Model | NZ2520SJ |
| Output Level |  |  |  | CMOS |
| Nominal Frequency Range |  |  | (MHz) | 5 to 40 |
| Overall Frequency Tolerance *1 |  |  | ( $\times 10^{-6}$ ) | $\pm 30$ |
| Operating Temperature Range |  |  | $\left({ }^{\circ} \mathrm{C}\right)$ | -40 to +85 |
| Supply Voltage [ Vcc ] |  |  | (V) | +1.8 $\pm 0.18$ |
| Current <br> Consumption Max. | During operation* ${ }^{2}$ | $+25^{\circ} \mathrm{C}$, No-load | (mA) | 0.55 to 0.70 |
|  |  | $+25^{\circ} \mathrm{C}, 15 \mathrm{pF}$ |  | 0.82 to 1.78 |
|  | During standby | $+25^{\circ} \mathrm{C}$, No-load/15pF | ( $\mu \mathrm{A}$ ) | 10 |
| VoıMax. / Voн Min. |  |  | (V) | $0.1 \mathrm{Vcc} / 0.9 \mathrm{Vcc}$ |
| Tr Max. / Tf Max. |  |  | (ns) | $8 / 8$ (at $0.1 \mathrm{~V}_{\mathrm{cc}}$ to $0.9 \mathrm{~V}_{\mathrm{cc}}$ ) |
| Symmetry Min. to Max. |  |  | (\%) | 45 to 55 (at 0.5 V cc) |
| Load (CL) Max. |  |  | (pF) | 15 |
| Start-up Time Max. |  |  | (ms) | 4 |
| Standby function |  |  |  | Available (Three-state) |
| Specification Number |  |  |  | NSA3608A |

*1 : Frequency Tolerance ( $+25^{\circ} \mathrm{C}$ ), Frequency/Temperature characteristics, Frequency/Voltage characteristics.
*2 : Consumption current at the time of loading capacity (CLout)on an output Icc (CLout) is consumption current(Icc) at the time of no-load, and output frequency. (Fout) It can ask by the following formula.
$\operatorname{Icc}\left(\mathrm{C}_{\text {Lout }}\right)[\mathrm{mA}]=\mathrm{Icc}[\mathrm{mA}]+\mathrm{C}_{\text {lout }}[\mathrm{pF}] \times \mathrm{V}_{\mathrm{cc}}[\mathrm{V}] \times \mathrm{Fout}[\mathrm{MHz}] \cdot 10^{-3}$


## Output Waveform <CMOS>



Standby Function

| \#1 Input | \#3 Output |
| :---: | :---: |
| Level $\mathrm{H}\left(0.7 \mathrm{~V}_{\mathrm{cc}} \leq \mathrm{V}_{\mathrm{IH}} \leq \mathrm{Vcc}\right)$ <br> or OPEN is selected. | Oscillation output ON |
| Level L (VII $\leq 0.3 \mathrm{Vcc})$ is selected. | High impedance |

