

# D3CE4R5ST

# Schottky Barrier Diodes 45V, 3A

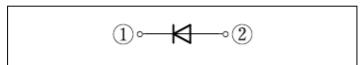
#### **Feature**

- Ultra-small SMD
- Ultra thin PKG
- Tj=175°C
- Ultra low I<sub>R</sub>
- Pb free terminal
- RoHS:Yes

#### **OUTLINE**

Package (House Name): CE
Package (JEITA Code): SC-110B

# **Equivalent circuit**



# Absolute Maximum Ratings (unless otherwise specified: Tl=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	Tstg		-55 to 175	°C
Junction temperature	Tj		-55 to 175	°C
Repetitive peak reverse voltage	V <sub>RRM</sub>		45	V
Average forward current	I <sub>F</sub> (AV)	50Hz sine wave, Resistance load, Tl=133°C	3	Α
Average forward current	I <sub>F</sub> (AV)	50Hz, Sine wave, Resistance load, Ta=25°C ※	2.04	Α
Surge forward current	I <sub>FSM</sub>	50Hz, Sine wave, Resistance load,Tj=25°C	90	Α

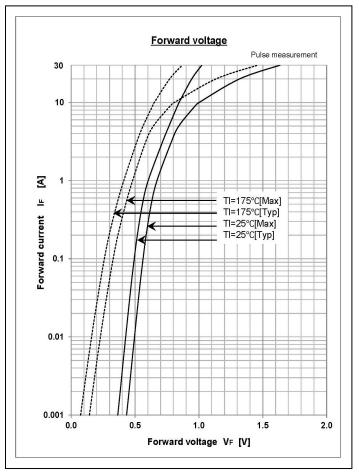
 $<sup>\</sup>divideontimes$  : See the original Specifications

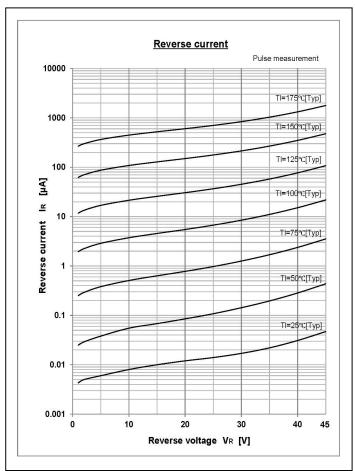
# **Electrical Characteristics** (unless otherwise specified: Tl=25°C)

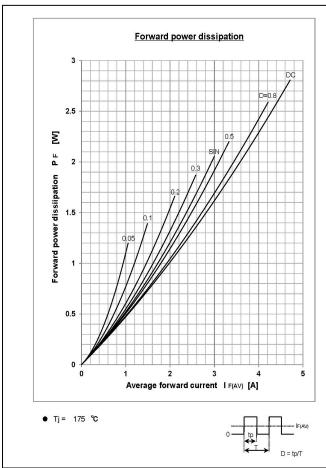
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	Unit
Forward voltage	$V_{F}$	IF=3A, Pulse measurement			0.74	٧
Reverse current	I <sub>R</sub>	VR=45V, Pulse measurement			0.008	mA
Total capacitance	Ct	f=1MHz, VR=10V		98		pF
Thermal resistance	Rth(j-l)	Junction to lead*			20	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient **			120	°C/W

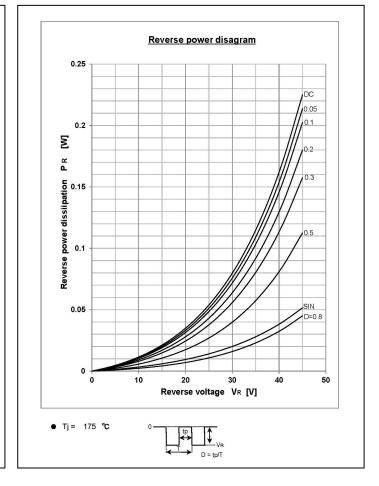
**<sup>※</sup>** ∶See the original Specifications

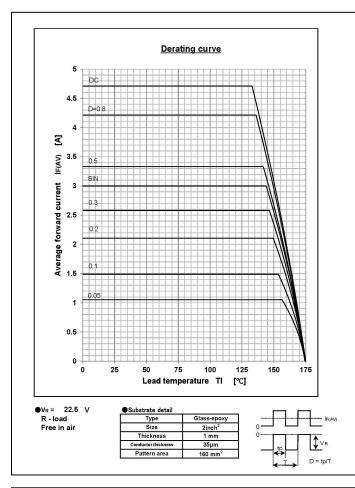
# **CHARACTERISTIC DIAGRAMS**

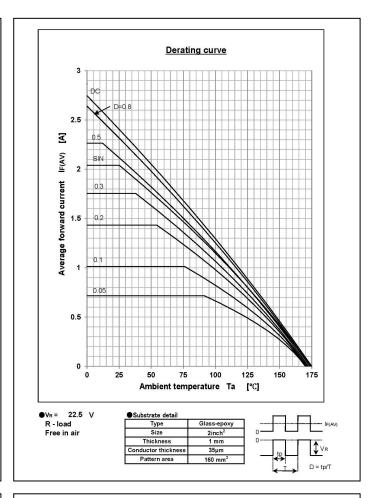


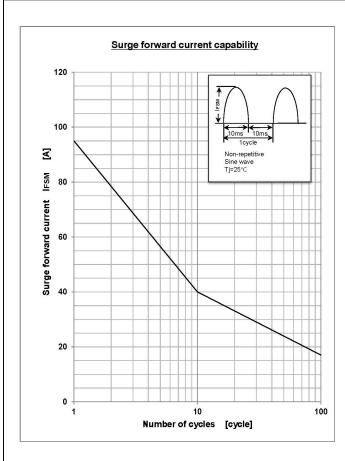


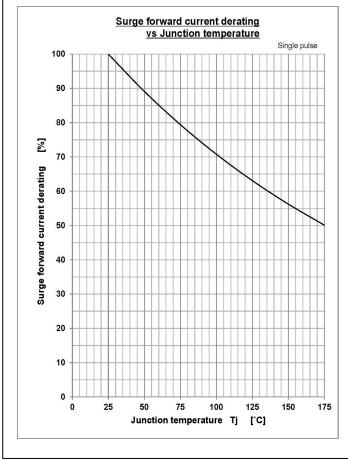


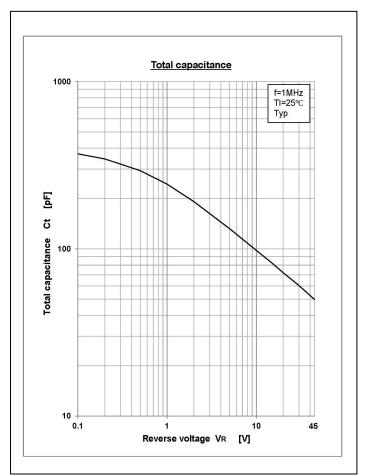


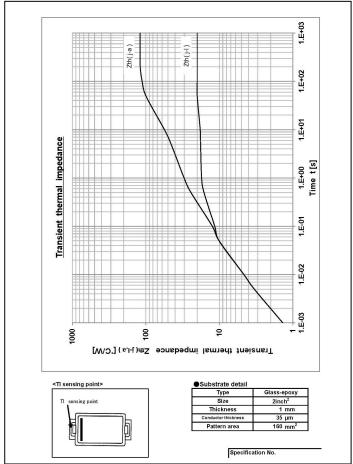










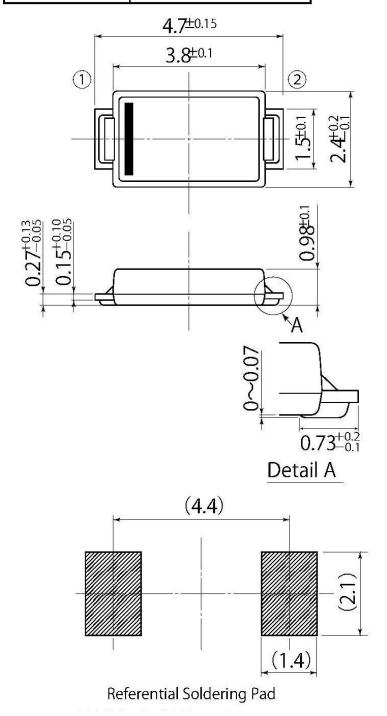


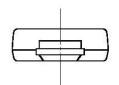
# unit:mm

scale: 10/1

**B**5

JEDEC Code	_
JEITA Code	SC-110B
House Name	CE





 $<sup>\</sup>bullet$  Optimize soldering pad to the board design and soldering condition.

#### **Notes**

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