

D3CE10ST
Schottky Barrier Diodes
100V, 3A

Feature

- Ultra-small SMD
- Ultra thin PKG
- Tj=175°C
- Ultra low I_R
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): CE
Package (JEITA Code): SC-110B



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tl=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	Tstg		-55 to 175	°C
Junction temperature	Tj		-55 to 175	°C
Repetitive peak reverse voltage	V _{RRM}		100	V
Average forward current	I _F (AV)	50Hz sine wave, Resistance load, Tl=124°C	3	A
Average forward current	I _F (AV)	50Hz, Sine wave, Resistance load, Ta=25°C ※	1.74	A
Surge forward current	I _{FSM}	50Hz, Sine wave, Resistance load,Tj=25°C	80	A

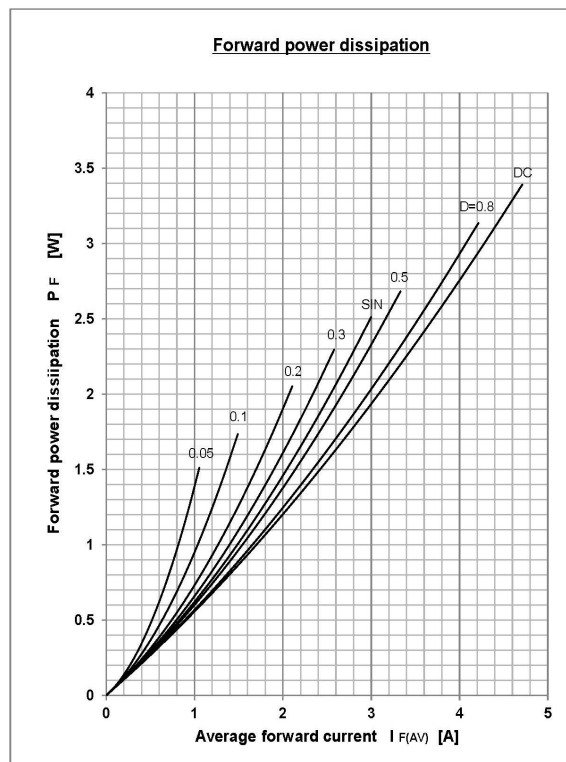
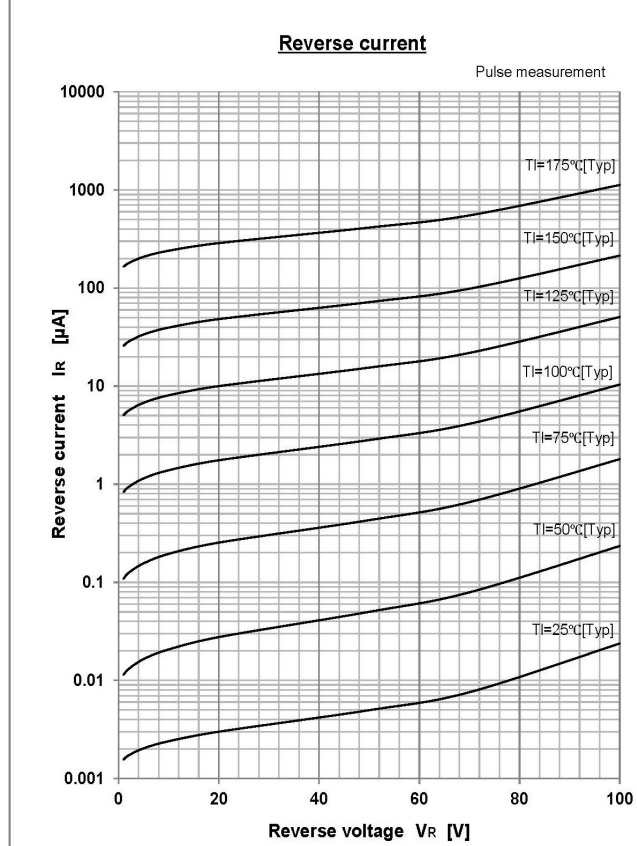
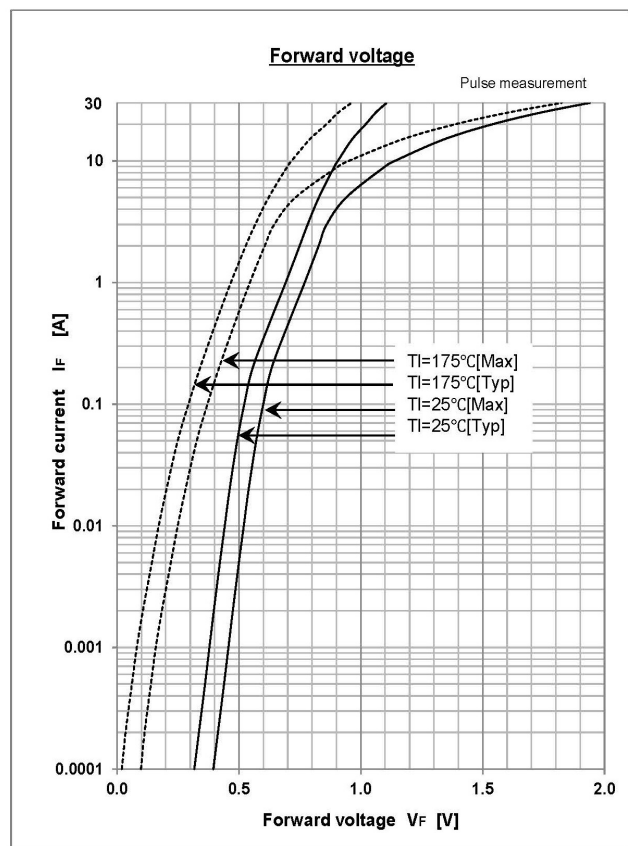
※ :See the original Specifications

Electrical Characteristics (unless otherwise specified : Tl=25°C)

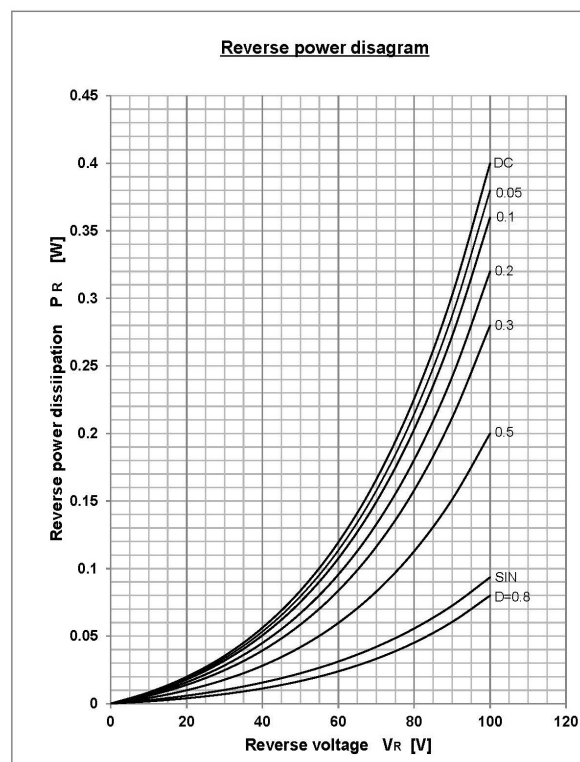
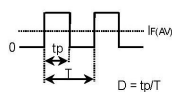
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Forward voltage	V _F	IF=3A, Pulse measurement			0.86	V
Reverse current	I _R	VR=100V, Pulse measurement			0.008	mA
Total capacitance	Ct	f=1MHz, VR=10V		60		pF
Thermal resistance	Rth(j-l)	Junction to lead※			20	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient ※			120	°C/W

※ :See the original Specifications

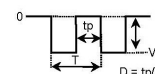
CHARACTERISTIC DIAGRAMS

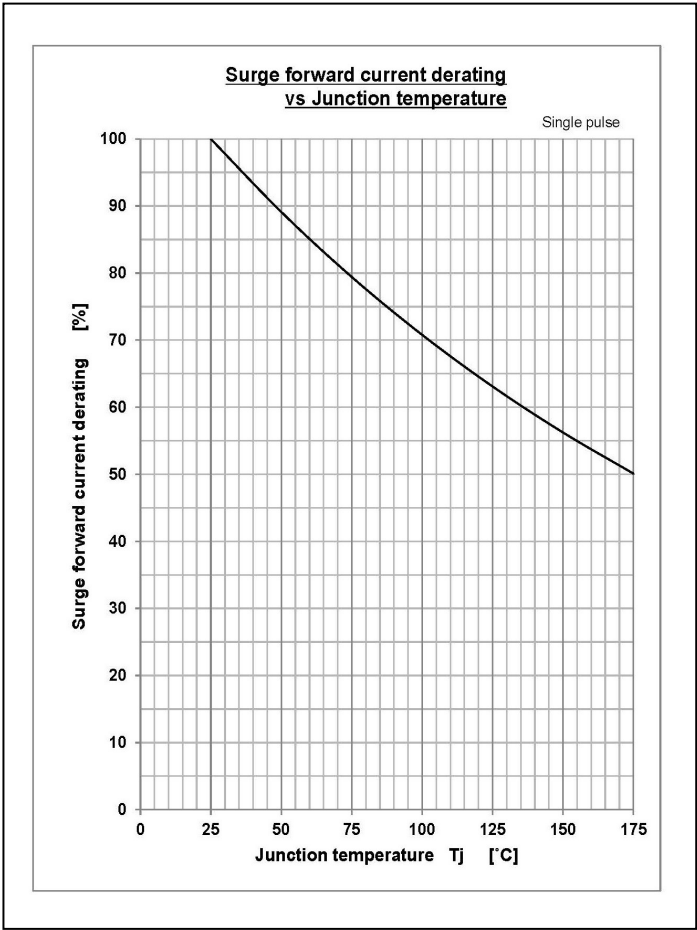
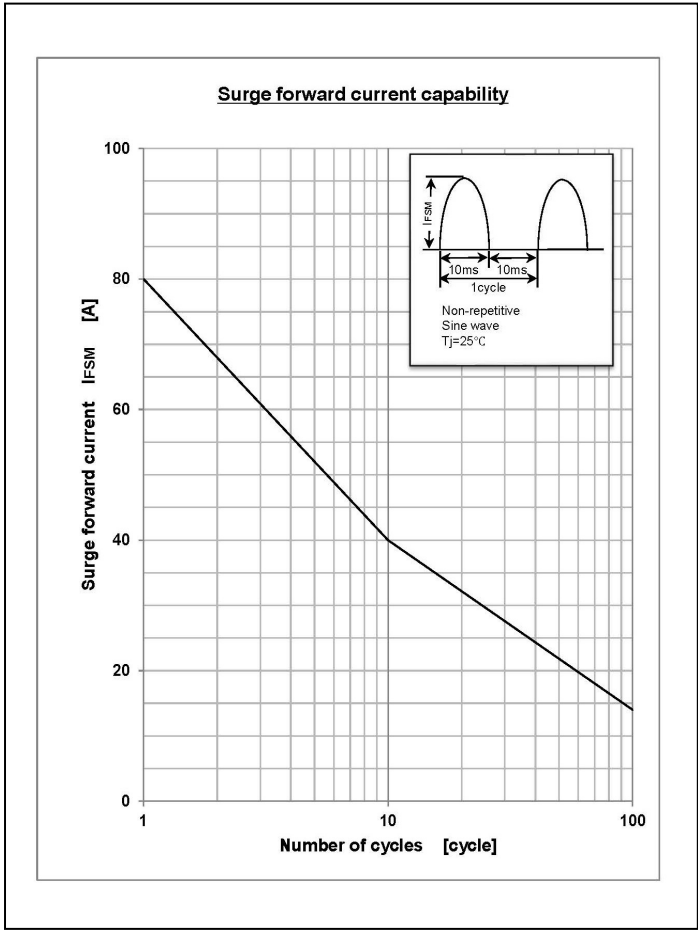
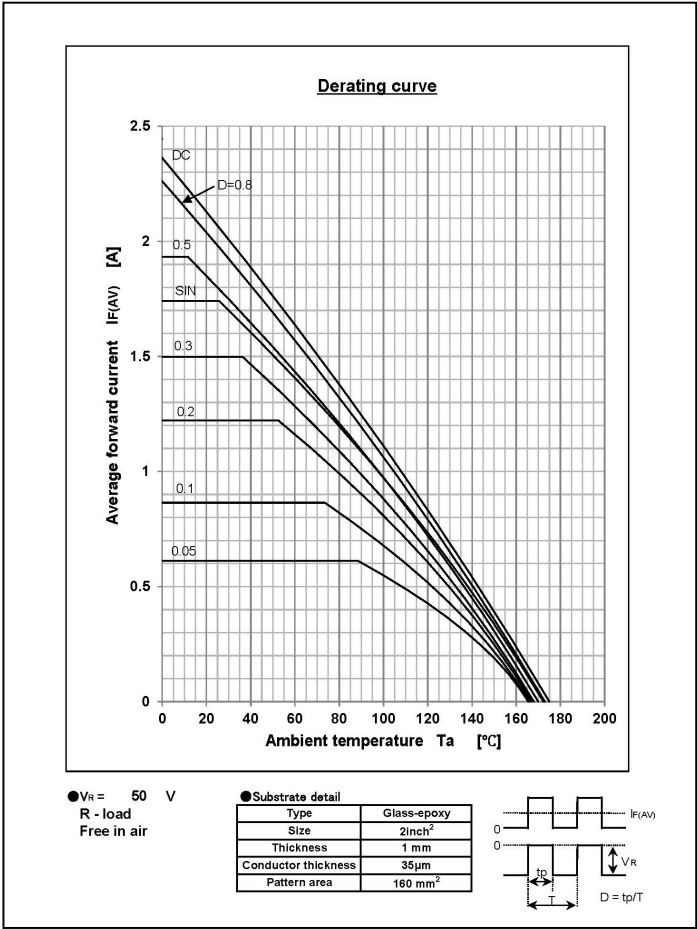
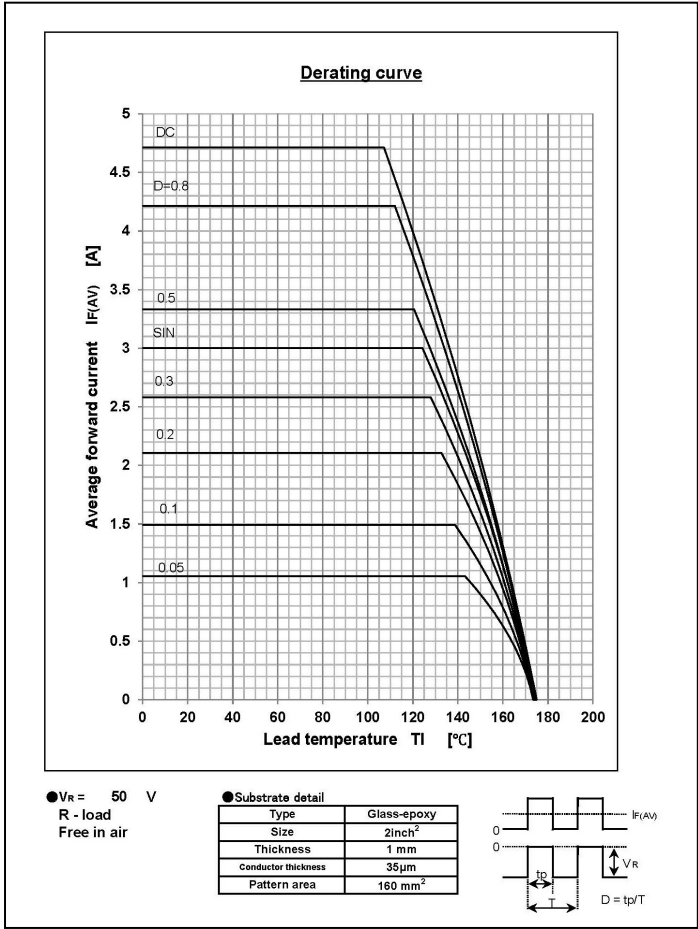


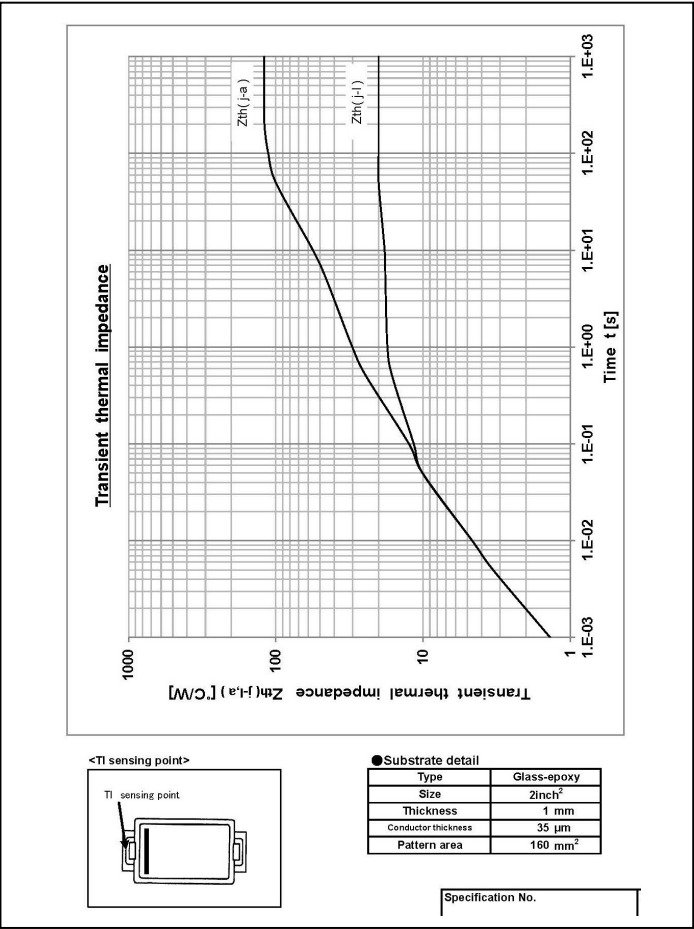
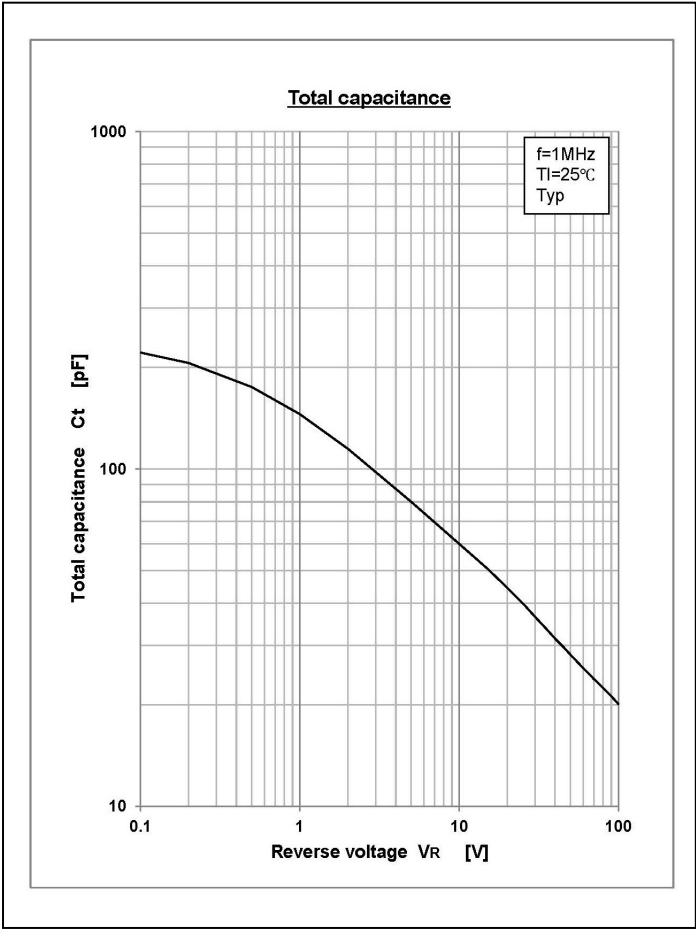
● $T_j = 175^\circ\text{C}$



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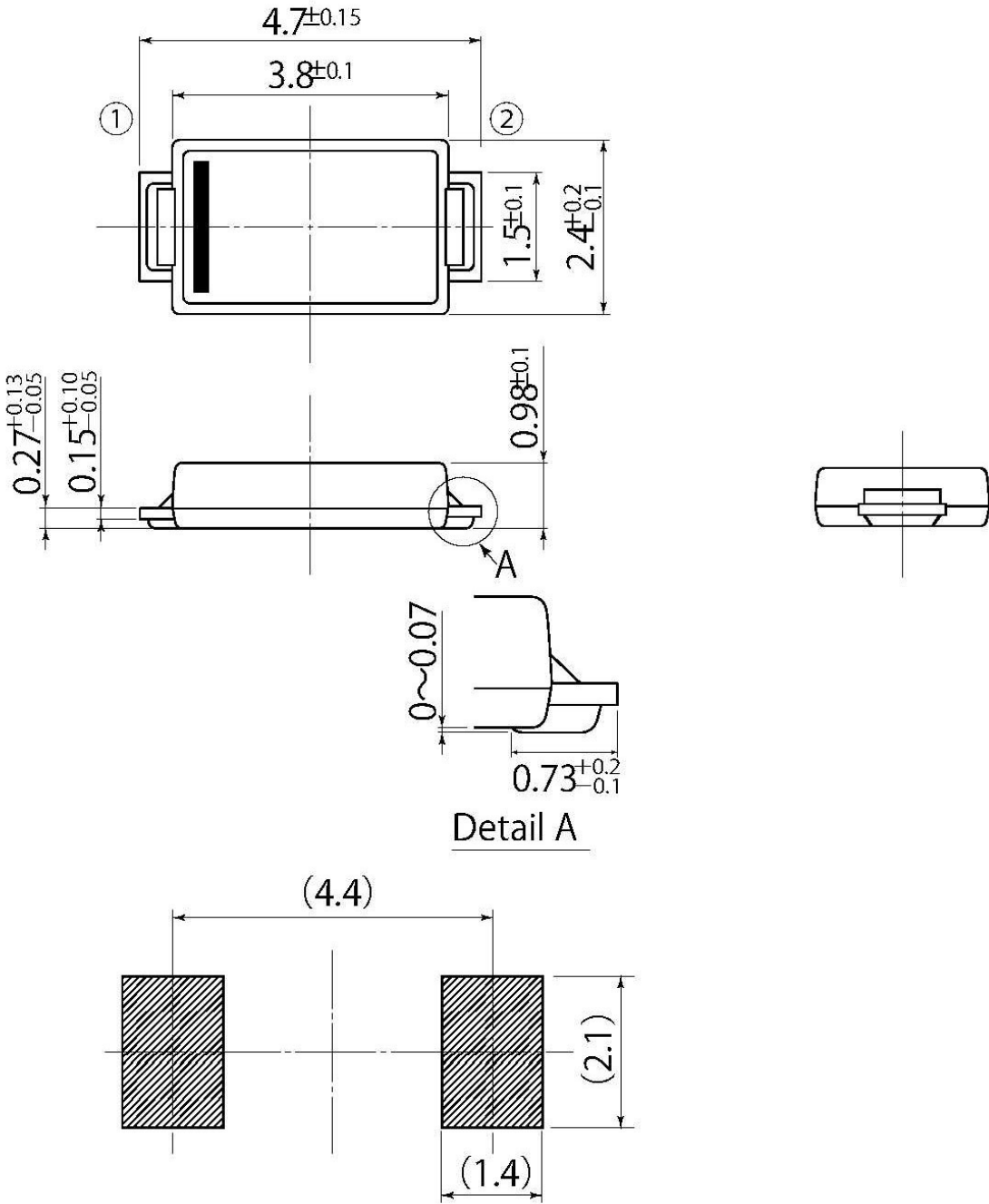






B5

JEDEC Code	—
JEITA Code	SC-110B
House Name	CE



• Optimize soldering pad to the board design and soldering condition.

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